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SERIAL NUMBER 10/616,981	FILING DATE 07/11/2003 RULE	1	LASS 257	GRO	JP ART (2815	JNIT	D	ATTORNEY OCKET NO. 1462.01108A
APPLICANTS								
Yuichiro Yamada, Kyoto-shi, JAPAN;								
Tsuyoshi Hamatani, Ohtsu-shi, JAPAN;								
** CONTINUING DATA **********************************								
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/16/2003								
Foreign Priority claimed	yes on o Met aff		STATE OR	SH	EETS	тот	AL	INDEPENDENT
35 USC 119 (a-d) conditions met Verified and CACknowledged Ex	Allowance Allowance	nttals	COUNTRY JAPAN	DRA	AWING 70	CLAI	-11.	CLAIMS 1
ADDRESS STEVENS, DAVIS, MILLER & MOSHER, L.L.P. Suite 850 1615 L Street, N.W. Washington , DC 20036								
TITLE								
Semiconductor chip, wiring board and manufacturing process thereof as well as semiconductor device								
	☐ All Fees							
1.16 Fees (Filing))		
FILING FEE FEES: Authority has been given in Paper 1.17 Fees (Processing Ext. of								ssing Ext. of

FILING FEE	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT	1.17 Fees (Processing Ext. of time)
RECEIVED 2154	No for following:	☐ 1.18 Fees (Issue)
2154		Other
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